Application No. 09/777,003 Amendment dated January 20, 2004 Reply to Office Action of August 21, 2003

and external I/O devices via output pins.

Amendments to the specification:

Please replace the abstract with the following new abstract:

circuit. A processor is disposed within the multimedia processor which controls the operation of the multimedia processor. A data transfer switch is disposed within the multimedia processor and coupled to the processor which transfers data to various modules of the multimedia processor. A fixed function unit is disposed within the multimedia processor, coupled to the processor and the data transfer switch. A data streamer is coupled to the data transfer switch, and configured to schedule simultaneous data transfers among a plurality of modules disposed within the multimedia processor in accordance with the corresponding channel allocations. As interface unit is coupled to the data streamer and has a plurality of I/0 device driver units. A multiplexer

coupled to the interface unit provides access between a selected number of I/0 device driver units

An integrated multimedia system has a multimedia processor disposed in an integrated